

UTKAL GOURAV MADHUSUDAN INSTITUTE OF TECHNOLOGY, RAYAGADA
Academic Lesson Plan for Summer semester- 2024

**Teaching Faculty: Mr. Kishore Chandra Prusty
 & Sri Arabinda Pradhan**

Department: Electrical Engineering

Semester: 6th

Subject: PROJECT-II(PRACTICAL)

No. of periods per week: 8

Total Periods: 120

End semester exam: 100

Sessional-50

Total Marks: 150

Sl. No.	Week	Period	Unit/Chapter	Topic to be covered
1.	1 st	1 st		Creating group and divided group members
2.		2 nd		
3.		3 rd		
4.		4 th		
5.		5 TH		Creating group and divided group members
6.		6 TH		
7.		7 TH		Creating group and divided group members
8.		8 TH		
9.	2 nd	1 st		Selection of project assignment
10.		2 nd		
11.		3 rd		
12.		4 th		
13.		5 TH		Selection of project assignment
14.		6 TH		
15.		7 TH		Selection of project assignment
16.		8 TH		
17.	3 rd	1 st		Planning and execution of considerations
18.		2 nd		
19.		3 rd		
20.		4 th		
21.		5 TH		Planning and execution of considerations
22.		6 th		
23.		7 th		Quality of performance
24.		8 th		
25.	4 th	1 st		Quality of performance
26.		2 nd		
27.		3 rd		
28.		4 TH		
29.		5 TH		Quality of performance
30.		6 th		
31.		7 th		Quality of performance
32.		8 th		
33.	5 th	1 st		Providing solution of the problems or production of final product
34.		2 nd		
35.		3 rd		
36.		4 th		Providing solution of the problems or production of final product
37.		5 th		
38.		6 th		
39.		7 th		Providing solution of the problems or production of final

40.		8 th		product
41.	6 th	1 st		Working on hardware model& simultaneously prepare report and program
42.		2 nd		
43.		3 rd		
44.		4 th		
45.		5 th		Working on hardware model& simultaneously prepare report and program
46.		6 th		
47.		7 th		
48.		8 th		
49.	7 th	1 st		Working on hardware model& simultaneously prepare report and program
50.		2 nd		
51.		3 rd		
52.		4 th		
53.		5 th		Working on hardware model& simultaneously prepare report and program
54.		6 th		
55.		7 th		Working on hardware model& simultaneously prepare report and program
56.		8 th		
57.	8 th	1 st		Working on hardware model& simultaneously prepare report and program
58.		2 nd		
59.		3 rd		
60.		4 th		
61.		5 th		Working on hardware model& simultaneously prepare report and program
62.		6 th		
63.		7 th		Working on hardware model& simultaneously prepare report and program
64.		8 th		
65.	9 th	1 st		Checking the project and supervise & suggest to improvise the accuracy of the report .
66.		2 nd		
67.		3 rd		
68.		4 th		
69.		5 th		Checking the project and supervise & suggest to improvise the accuracy of the report .
70.		6 th		
71.		7 th		Checking the project and supervise & suggest to improvise the accuracy of the report .
72.		8 th		
73.	10 th	1 st		Checking the project and supervise & suggest to improvise the accuracy of the report .
75.		2 nd		
76.		3 rd		
77.		4 th		
77.		5 th		Checking the project and supervise & suggest to improvise the accuracy of the report .
78.		6 th		
79.		7 th		Sense of responsibility
80.		8 th		
81.	11 th	1 st		Sense of responsibility
82.		2 nd		
83.		3 rd		
84.		4 th		
85.		5 th		Sense of responsibility
86.		6 th		
87.		7 th		Sense of responsibility
88.		8 th		
89.	12 th	1 st		Testing on hardware model & simultaneously working program.
90.		2 nd		

91.	13 th	3 rd		
92.		4 th		Testing on hardware model & simultaneously working program.
93.		5 th		
94.		6 th		
95.		7 th		Testing on hardware model & simultaneously working program.
96.		8 th		
97.		1 st		Testing on hardware model & simultaneously working program.
98.		2 nd		
99.		3 rd		
100.	14 th	4 th		Testing on hardware model & simultaneously working program.
101.		5 th		
101.		6 th		
102.		7 th		
104.		8 th		Testing on hardware model & simultaneously working program.
105.		1 st		
106.		2 nd		Trial Presentation and Mock viva.
107.		3 rd		
108.	15 th	4 th		Trial Presentation and Mock viva.
109.		5 th		
110.		6 th		
111.		7 th		Trial Presentation and Mock viva.
112.		8 th		
113.		1 st		Sessional.
114.		2 nd		
115.		3 rd		
116.	16 th	4 th		Sessional.
117.		5 th		
118.		6 th		
119.		7 th		
120.		8 th		Sessional.

The lesson plan prepared by the concerned faculty :

**Mr. Kishore Chandra Prusty
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Department of Electrical Engineering.