

UTKAL GOURAV MADHUSUDAN INSTITUTE OF TECHNOLOGY, RAYAGADA
Academic Lesson Plan for summer semester- 2023(s)

Name of the teaching faculty : **Sarita Bauri**
 Semester:4th
 No. of periods per week: **3**
 End Semester Examination-50

Discipline / Dept.: **EE**
 Subject(PRATICAL): **AE**
 Total Period :45
 Sessional : 25

Week	Period	UNIT/CHAPTER	TOPIC TO BE COVERED
1 ST	1 st	EXPERIMENT 1	Determine the input and output Characteristics of CE & CB transistor configuration
	2 nd		
	3 rd		
2 ND	1 st	EXPERIMENT2	Determine Drain & Transfer Characteristics of JFET
	2 nd		
	3 rd		
3 RD	1 st	EXPERIMENT3	Construct Bridge Rectifier using different filter circuit and to determine Ripple factor & analyze wave form with filter & without filter
	2 nd		
	3 rd		
4 TH	1 st	EXPERIMENT4	Construct Bridge Rectifier using different filter and to determine Ripple factor
	2 nd		
	3 rd		
5 TH	1 st	EXPERIMENT5	Construct & test the regulator using Zener diode
	2 nd		
	3 rd		
6 TH	1 st	EXPERIMENT6	Construct different types of biasing circuit and analyze the wave form (i) Fixed bias (ii) Emitter bias (iii) Voltage divider bias
	2 nd		
	3 rd		
7 TH	1 st	EXPERIMENT7	Study the single stage CE amplifier & find Gain
	2 nd		
	3 rd		
8 TH	1 st	EXPERIMENT8	Study multi stage R-C coupled amplifier & to determine frequency- response & gain
	2 nd		
	3 rd		
9 TH	1 st	EXPERIMENT9	. Construct & Find the gain of Class A. Amplifier
	2 nd		