U. G. M. I. T, RAYAGADA								
DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGG								
ACADEMIC LESSON PLAN FOR SUMMER SEMESTER-2023								
NAME OF THE FACU Smita Patnaik DEP					ETC			
SEMESTER			4th	SUBJECT		Analog Electronics and IC Lab		
NO. OF PERIODS PER WEEK			4	TOTAL PERIODS		60		
END SEMESTER EXAM			50	SESSIONAL		50		
TOTAL MARKS 100								
WEEK PERIOD G			JIDE			TOPIC TO BE COVERED		
1st	2				Determine the forward and reverse bias of diode			
	2				construction bridge rectifier			
2nd	2				Determined the ripple factor			
	2				Determined the input a			
3rd	2				Determined output characteristics of CE			
	2				Transistor regulator using zener diode			
4th	2				Study the two stage amplifier			
	2				Find the gain of class A			
5th	2			Find the gain amplifier ofClassB				
	2	Construction of pushpull amplifier						
6th	2				Determine thedrain of JFET			
					Construction and calculation of frequency of heartly oscillator			
7th	2	LAB PRACTICE WITH USING LAB MANUAL AND RECORD MAINTENANCE			Find frequency of collpit oscillator			
	2				Find frequency of wein bridge oscillator			
8th	2				Find the frequency of phase shift oscillator			
	2				construction ic 555			
9th	2				waveform of clippers circuit			
	2				waveform of clamper circuit			
10th	2				Test voltage power supply of 78xx			
	2				Test voltage power supply of LM723			
11th	2				Study of 741 ic			
4211					study of inverting amplifier			
12th	2				Study of integrator			
1211	2				study of window comparator			
13th	2				study of summing amplifier			
1.4+6	2				study of differentiator amplifier VI characteristics of zener diode			
14th	2				Test voltage power supply of 79xx			
15+h	2				Study of sub	•		
15th	Z					istractor a	mpillier	